

A Fast and Rigorous CAD Procedure for Complex Shielded Planar Circuits

G.V. Eleftheriades, H. Le Pezennec and J.R. Mosig. "A Fast and Rigorous CAD Procedure for Complex Shielded Planar Circuits." 1996 MTT-S International Microwave Symposium Digest 96.3 (1996 Vol. III [MWSYM]): 1467-1470.

An efficient Integral-Equation/Method of Moments (MoM) technique is presented for the analysis of shielded planar circuits (MIC's and MMIC's). Unlike traditional approaches which implicate the Fast Fourier Transform (FFT) to compute the slowly converging MoM matrix elements, this technique is not restricted to uniform meshes. This feature is exploited by introducing a new modular meshing strategy with rectangular cells. The entire formulation is extended to multilayer/multiconductor substrates.

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